

## JUMPER AND LED CHART FOR PMC ADAPTERS

8/7/00  
400-0020-001

CATALYST BOARD TYPE	LEDS		JUMPERS			
	+5V	BUS MODE 1	BUS MODE 2	BUS MODE 3	BUS MODE 4	OTHER
PCI2PMC-X	D1X	N/A	N/A	N/A	N/A	
PCI2PMC-XRT	D1X	N/A	N/A	N/A	N/A	J1 – P4 ODD PINS J4 – P4 EVEN PINS
PMC2PCI-X (PMC2PCI-A)	D1	D20	JPN2	JPN3	JPN4	
PMC2C-X	N/A	D20	JPN2	JPN3	JPN4	
C2PMC-X (PMCPMC2CPCI-64)	N/A	D20B	JPN2B	JPN3B	JPN4B	

LED	+5V	When lit, indicates the presence of +5V
LED	BUS MODE 1	Is an output used by the PMC Card to indicate its presence in the system. See Table 2.
HEADER	BUS MODE 2, BUS MODE 3, BUS MODE 4	Used to determine the encoding of the bus mode. See Table 1 below.

BUS MODE 4	BUS MODE 3	BUS MODE 2	MODE
L	L	L	Card Present Test: “Card Present” only, if a card is plugged into the slot, no bus protocol should be used.
L	L	H	Return “Card Present” if PCI capable and uses PCI protocol
L	H	L	Reserved
L	H	H	Reserved
H	L	L	Reserved
H	L	H	Reserved
H	H	L	Reserved
H	H	H	No Host Present

**TABLE 1**

LOW – Jumper installed  
HIGH – Jumper not installed

BUSMODE [4:2]	PMC Card Capabilities	BUSMODE 1	LED
L L L	Independent of PMC Card	L	LIT
L L H	Capable of performing PCI protocol	L	LIT
L L H	Incapable of performing PCI Protocol	H	OFF
L H L	Capable of performing “Reserved” Protocol	L	LIT
L H L	Incapable of performing “Reserved” Protocol	H	OFF
L H H -> H H L	Independent of PMC Card	H	OFF
H H H	Independent of PMC Card	H	OFF

**TABLE 2**

LOW – LED lit  
HIGH – LED off

\* The Bus Mode signal definition listed in tables 1 & 2 above, were extracted from the PMC Specification Draft 2.2 Dated 22-Apr-00.